Solving the region growing problem on the Connection Machine

Nawal Copty
Syracuse University, School of Computer and Information Science, nkcopty@top.cis.syr.edu

Sanjay Ranka
Syracuse University, School of Computer and Information Science, ranka@top.cis.syr.edu

Geoffrey C. Fox
Syracuse University, School of Computer and Information Science

Ravi Shankar
Syracuse University, School of Computer and Information Science, rshankar@top.cis.syr.edu

Follow this and additional works at: https://surface.syr.edu/lcsmith_other

Recommended Citation
Cooty, Nawal; Ranka, Sanjay; Fox, Geoffrey C.; and Shankar, Ravi, "Solving the region growing problem on the Connection Machine" (1993). College of Engineering and Computer Science - Former Departments, Centers, Institutes and Projects. 35.
https://surface.syr.edu/lcsmith_other/35

This Article is brought to you for free and open access by the College of Engineering and Computer Science at SURFACE. It has been accepted for inclusion in College of Engineering and Computer Science - Former Departments, Centers, Institutes and Projects by an authorized administrator of SURFACE. For more information, please contact surface@syr.edu.
Solving the Region Growing Problem on the Connection Machine

Nawal Copty, Sanjay Ranka, Geoffrey Fox, and Ravi Shankar

School of Computer and Information Science
Syracuse University, Syracuse, NY 13244

Email: nkcopsty, ranka, rshankar@top.cis.syr.edu, gcf@npacs.syr.edu

Published in:

Abstract — This paper presents a parallel algorithm for solving the region growing problem based on the split and merge approach. The algorithm was implemented on the CM-2 and the CM-5 in the data parallel and message passing models. The performance of these implementations is examined and compared.

Keywords: Region growing, Split and merge, Data parallel, Message passing, Connection machine.

The Region Growing Problem
Region growing is a general technique for image segmentation. Image characteristics are used to group adjacent pixels together to form regions. Regions are then merged with other regions to grow larger regions. A region might correspond to a world object or a meaningful part of one [2].

The merging of pixels or regions to form larger regions is usually governed by a homogeneity criterion that must be satisfied. A variety of homogeneity criteria have been investigated for region growing. The pixel range homogeneity criterion requires that the difference between the minimum and maximum intensities within a region not exceed a threshold value \( T \).

There are many approaches for solving the region growing problem [1, 2, 10]. This paper presents a parallel algorithm for solving the problem based on the split and merge approach [5]. The algorithm aims to reduce the number of merge steps required to identify the regions in the image by using a preprocessing split stage.

While previous parallel implementations of the split and merge approach have used dynamic or tree structures to represent the regions in the image [8, 9], our implementations use only one and two-dimensional arrays to solve the problem. Moreover, we introduce an element of randomness to the merging of regions. For a detailed presentation, refer to [3].

The Split and Merge Approach
The split and merge approach solves the region growing problem in two stages: the split stage and the merge stage.

The Split Stage
In the split stage, an \( N \times N \) image is partitioned into square regions which satisfy the homogeneity criterion. At first, each pixel is considered a homogeneous square region of size \( 1 \times 1 \). Then every group of four adjacent pixels is tested for homogeneity. If the homogeneity criterion is satisfied, the pixels are combined into one larger square region of size \( 2 \times 2 \), and so on. The split stage terminates when the whole image is one square region of size \( N \times N \), or when no more square regions can be merged. Figure 1 shows the square regions produced by the split stage for a \( 4 \times 4 \) image, where the threshold value \( T = 3 \).

\begin{align*}
\text{(a)} & : \\
\text{(b)} & : 
\end{align*}

Figure 1: The Split Stage

The Merge Stage
In the merge stage, the square regions are iteratively merged into larger and larger regions which satisfy the homogeneity criterion. The merge continues until no more merges are possible.

The merge is achieved by reformulating the region growing problem as a weighted, un-directed graph problem, where the vertices of the graph represent the regions in the image, and the edges represent the neighboring relationships among these regions. That is, an edge \( e \) exists between two vertices \( v \) and \( w \) of the graph, if and only if the regions represented by \( v \) and \( w \) share a common boundary. The weight of the edge \( e \) is the difference between the maximum and minimum pixel values in the union of the two regions represented by \( v \) and \( w \).

Obviously, only vertices connected by edges satisfying the homogeneity criterion can be merged. In one merge iteration, each region selects for merging its neighbor that best satisfies the homogeneity criterion.
A tie may be broken by selecting the neighbor with the smallest (largest) ID, or by selecting a neighbor at random. Two regions actually merge if they select each other for merging. Once two regions merge, the vertices and edges of the graph are updated to reflect the new regions in the image.

Figure 2 shows the different regions obtained and their corresponding graphs in each iteration of the merge stage, for the $4 \times 4$ image of Figure 1. Ties are broken by selecting the neighbor with the smallest ID. The small numbers appearing in the upper left-hand corners of the regions denote the region IDs.

![Regions](image)

Regions: (a) at start of the merge stage; (b) after first merge iteration; (c) after second merge iteration; (d) after third and final merge iteration

**Data Parallel Implementation**

The data parallel implementation of the split and merge algorithm consists of the following steps:

1. The two-dimensional pixel image is repeatedly split into homogeneous square regions. The split stage stops when the whole image is one homogeneous square region, or when no more merges are possible.
2. For each square region in the pixel image, a corresponding graph vertex is created, and for each pair of neighboring square regions, an edge is created. Edges that do not satisfy the homogeneity criterion are deactivated.
3. A region determines its neighboring region that best satisfies the homogeneity criterion. In the case of a tie, one of the neighboring regions is chosen at random. Two regions merge if their merge choices are mutual. In one merge iteration, several region pairs can merge at the same time without conflicting with each other.
4. When two regions merge, the region with the smaller ID becomes the representative of the two. The vertices and edges of the graph are updated to reflect the new regions in the image. Edges that do not satisfy the homogeneity criterion are deactivated.
5. If there still exist active edges, then steps 3 and 4 are repeated. Otherwise, the program terminates.

**Message Passing Implementation**

The message passing implementation of the split and merge algorithm is a hand-coded translation of the data parallel one and consists of the following steps:

1. The image is mapped to the node processor grid such that each processor receives an $\frac{N}{P_1} \times \frac{N}{P_2}$ sub-image of the original image. This partitioning maintains adjacency between neighboring blocks of the image.
2. Each node processor splits independently its $\frac{N}{P_1} \times \frac{N}{P_2}$ sub-image and determines the homogeneous square regions within it.
3. Each node processor sets up the vertices and edges of the graph associated with its sub-image. Boundary information is exchanged so that edges connected to vertices in other processors are created.
4. The node processors cooperate to merge the homogeneous square regions.
5. The node processors cooperate to update the vertices and edges of their graphs.
5. If there still exist any active edges in any of the node processors, then steps 3 and 4 are repeated. Otherwise, the node programs terminate.

At several points in the message passing implementation, irregular communication is required, where each of the node processors sends zero or more messages to other processors in an irregular fashion.

Two different communication schemes were investigated. The first, called Linear Permutation (LP) [7], uses synchronous (blocked) message passing. In this scheme, each node obtains a copy of the communication matrix, using a global concatenation operation. Then in step \( i \), \( 0 < i < Q \), processor \( p_i \) sends a message to processor \( p_{(i+1) \mod Q} \), and receives a message from processor \( p_{(i-k) \mod Q} \), where \( Q \) is the total number of node processors. The second communication scheme uses asynchronous message passing.

Resolving Ties at Random

In order to achieve a higher degree of parallelism, we introduced an element of randomness in our parallel implementations. In case of a tie during the merge stage, the tie is broken by selecting a neighbor at random instead of selecting the neighbor with the smallest (largest) ID, since the latter approach imposes a serialization on the order of the merges. The random approach in breaking ties was shown to be significantly faster than the approach of selecting the neighbor with the smallest (largest) ID, since it generally results in a larger number of merges per merge iteration.

Complexity

Given an \( N \times N \) pixel image, the complexity of the parallel split and merge algorithm depends on the number of processors (\( P \)) used and the number of iterations required to find the regions in the image.

The Split Stage: In the best case, when every pixel is a region by itself, only one split iteration is required. In the worst case, when the whole image is one homogeneous square region, \( \log(N) \) split iterations are required.

The time complexity of the split stage in the data parallel implementation on the CM-2 is \( O(k^2 + \log P) \), while that of the data parallel and message passing implementations on the CM-5 is \( O(k^2 + (\tau \times \log P)) \), where \( \tau \) is the communication set up time for one split iteration.

The Merge Stage: In the best case, a region consisting of \( R \) sub-regions will require \( \log R \) iterations to merge. In the worst case, when only one pair of regions is merged in each iteration, it will require \( R - 1 \) merge iterations.

Let \( R_i \) denote the number of homogeneous square regions found in the image at the end of the split stage, and let \( R_t \) denote the number of regions found at the end of the merge stage. If we assume that the number of regions is reduced by a factor of \( k \) at every step in the merge stage, then the time complexity of the merge stage in the data parallel implementation on the CM-2 is \( O(R_t \times \log R_t + \log k \times \log R_t) \). For details of the analysis and assumptions made, refer to [3].

The time complexity of the the data parallel and message passing implementations on the CM-5, on the other hand, is difficult to analyze, as the number of messages sent by the processors in each step of the merge stage depends on the image.

Performance

The data parallel implementation (using CM Fortran) of the split and merge algorithm was executed on both a 16K CM-2 and a 32-node CM-5, while the message passing implementation (using F77 + CMMD) was executed on a 32-node CM-5. A variety of images were used. The performance of the implementations for images of sizes \( 128 \times 128 \) and \( 256 \times 256 \) is presented below. LP refers to the Linear Permutation communication scheme and Async refers to the asynchronous one.

**Image 1: 128 \times 128** image composed of two nested rectangular regions

<table>
<thead>
<tr>
<th></th>
<th>split (secs)</th>
<th>split (secs)</th>
<th>Merge (secs)</th>
<th>Merge (secs)</th>
</tr>
</thead>
<tbody>
<tr>
<td>CM Fortran on:</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CM-2 (4K proc)</td>
<td>0.200</td>
<td>4</td>
<td>9.343</td>
<td>19</td>
</tr>
<tr>
<td>CM-2 (16K proc)</td>
<td>0.112</td>
<td>4</td>
<td>7.627</td>
<td>30</td>
</tr>
<tr>
<td>CM-5 (32 nodes)</td>
<td>0.361</td>
<td>4</td>
<td>33.013</td>
<td>19</td>
</tr>
<tr>
<td>F77 + CMMD on:</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CM-5 (32 nodes)</td>
<td>0.622</td>
<td>4</td>
<td>6.914</td>
<td>24</td>
</tr>
<tr>
<td>CM-5 (32 nodes, Async)</td>
<td>0.621</td>
<td>4</td>
<td>4.685</td>
<td>20</td>
</tr>
</tbody>
</table>

**Image 2: 128 \times 128** image composed of a collection of rectangles

<table>
<thead>
<tr>
<th></th>
<th>split (secs)</th>
<th>split (secs)</th>
<th>Merge (secs)</th>
<th>Merge (secs)</th>
</tr>
</thead>
<tbody>
<tr>
<td>CM Fortran on:</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CM-2 (4K proc)</td>
<td>0.260</td>
<td>4</td>
<td>8.384</td>
<td>19</td>
</tr>
<tr>
<td>CM-2 (16K proc)</td>
<td>0.112</td>
<td>4</td>
<td>5.345</td>
<td>17</td>
</tr>
<tr>
<td>CM-5 (32 nodes)</td>
<td>0.360</td>
<td>4</td>
<td>31.615</td>
<td>20</td>
</tr>
<tr>
<td>F77 + CMMD on:</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CM-5 (32 nodes)</td>
<td>0.622</td>
<td>4</td>
<td>9.296</td>
<td>35</td>
</tr>
<tr>
<td>CM-5 (32 nodes, Async)</td>
<td>0.621</td>
<td>4</td>
<td>6.404</td>
<td>35</td>
</tr>
</tbody>
</table>

**Image 3: 128 \times 128** image composed of a collection of circles

<table>
<thead>
<tr>
<th></th>
<th>split (secs)</th>
<th>split (secs)</th>
<th>Merge (secs)</th>
<th>Merge (secs)</th>
</tr>
</thead>
<tbody>
<tr>
<td>CM Fortran on:</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CM-2 (4K proc)</td>
<td>0.260</td>
<td>4</td>
<td>13.911</td>
<td>24</td>
</tr>
<tr>
<td>CM-2 (16K proc)</td>
<td>0.112</td>
<td>4</td>
<td>9.538</td>
<td>25</td>
</tr>
<tr>
<td>CM-5 (32 nodes)</td>
<td>0.360</td>
<td>4</td>
<td>42.507</td>
<td>27</td>
</tr>
<tr>
<td>F77 + CMMD on:</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CM-5 (32 nodes)</td>
<td>0.622</td>
<td>4</td>
<td>9.454</td>
<td>33</td>
</tr>
<tr>
<td>CM-5 (32 nodes, Async)</td>
<td>0.621</td>
<td>4</td>
<td>5.516</td>
<td>28</td>
</tr>
</tbody>
</table>

**Image 4: 256 \times 256** image composed of two nested rectangular regions

<table>
<thead>
<tr>
<th></th>
<th>split (secs)</th>
<th>split (secs)</th>
<th>Merge (secs)</th>
<th>Merge (secs)</th>
</tr>
</thead>
<tbody>
<tr>
<td>CM Fortran on:</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CM-2 (4K proc)</td>
<td>1.005</td>
<td>5</td>
<td>13.962</td>
<td>36</td>
</tr>
<tr>
<td>CM-2 (16K proc)</td>
<td>0.625</td>
<td>5</td>
<td>16.381</td>
<td>28</td>
</tr>
<tr>
<td>CM-5 (32 nodes)</td>
<td>2.052</td>
<td>5</td>
<td>37.388</td>
<td>25</td>
</tr>
<tr>
<td>F77 + CMMD on:</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CM-5 (32 nodes)</td>
<td>0.607</td>
<td>5</td>
<td>16.512</td>
<td>37</td>
</tr>
<tr>
<td>CM-5 (32 nodes, Async)</td>
<td>0.607</td>
<td>5</td>
<td>10.942</td>
<td>29</td>
</tr>
</tbody>
</table>
The bar chart of Figure 3 gives a visual comparison of the times taken by the merge stage in the various implementations.

![Figure 3: Comparison of Times Taken by the Merge Stage (Images 1-6)](image_url)

**Observations**

In examining the performance of the different parallel implementations of the split and merge algorithm, we make the following observations:

- The number of merge iterations required to find the regions in an image are not identical in all cases. The random numbers generated, as well as the order in which messages are received affect the actual merges that take place and hence the number of merge iterations required to solve the problem.

- Asynchronous communication on the CM-5 is faster than Linear Permutation, since in Linear Permutation the nodes must loop a larger number of times to complete the required communications.

- The CM Fortran version on the CM-2 runs faster than that on the CM-5. The SIMD hardware of the CM-2 directly supports the data parallel model, while compilers, assemblers, and other system software of the CM-5 have to deal with the many “housekeeping” details such as load balance and synchronization.

- The message passing implementation runs significantly faster than the data parallel one on the CM-5. The data parallel implementation relies on the CM Fortran compiler as well as the run-time system to lay out the data and to provide communication among the nodes, while, in the message passing implementation, the programmer exercises control over synchronization, data partitioning, and load balancing.

- CM Fortran allows only limited ways of distributing data on different processors. With the availability of new data distribution directives in High Performance Fortran, the performance of the data parallel implementation is expected to be closer to the message passing one.

**Acknowledgements:** We would like to thank Paul Coddington, Pablo Tamayo, and Jhy-Chun Wang for interesting and helpful discussions, and Gregor von Laszewski for help in preparing the manuscript.

**References**


